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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/562,891	12/29/2005	Marinus Adrianus Henricus Looijkens	NL 030765	6936
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EXAMINER				
CHOW, LIXI				
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2627				
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary**Application No.**

10/562,891

Applicant(s)LOOIJKENS, MARINUS
ADRIANUS HENRICUS**Examiner**

Lixi Chow

Art Unit

2627

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-10 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-10 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 29 December 2005 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. ____.
 3. ☒ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date See Continuation Sheet
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date ____.
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: ____

Continuation of Attachment(s) 3). Information Disclosure Statement(s) (PTO/SB/08), Paper No(s)/Mail Date :-----> 3/22/07.

DETAILED ACTION

Drawings

1. Figure 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Objections

2. Claim 1-10 are objected to because of the following informalities: the word "circuit" on line 12 of claim 1 should be --circuit--; and the numbers and the names of signals provided in the parentheses of all claims should be omitted. Appropriate correction is required.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 1-10 are rejected under 35 U.S.C. 102(b) as being anticipated by Davis et al. (US 5,809,006; hereafter Davis).

Regarding claim 1:

Davis discloses an optical writing system (see Fig. 8) for an optical disc writing apparatus, comprising:

an encoder device (see Fig. 3, element 810 inherently includes an encoder) having an input for receiving a data signal and an output for providing a single encoded signal (see Fig. 8, signal 212 is an encoded signal) which contains data information and clock information;

a laser driver circuit (see Fig. 8, elements 846, 240 and 241) having a signal input for receiving an encoded signal from the encoder device and comprising a flipflop device (see Fig. 12, element 1212) with a data input for receiving a digital data signal (see Fig. 12; the binary signal outputted from EFM encoder is a digital data signal), and a clock input (the channel clock signal is the clock signal) for receiving a digital clock signal, wherein the laser driver circuit further comprises signal generator means (Fig. 12, element 1210 is a signal generator means) having a signal input coupled to the signal input of the driver circuit (signal 212 is being inputted into the EFM decoder which is the signal generator means), a data output (the binary signal outputted from the EFM decoder is being inputted into the flipflop) coupled to the data input of the flipflop, and a clock output coupled to the clock input of the flipflop (channel clock signal is being inputted to the flipflop); the signal generator means being designed to generate at its data and clock outputs a digital data signal and a digital clock signal, respectively, from an encoded signal received at its signal input (see Fig. 12, the encoder is capable of extracting the clock signal from the EFM signal).

Regarding claim 2:

Davis discloses the optical writing system according to claim 1, wherein the encoder device is designed to generate at its output a digital data signal (see Fig. 8; signal 212 is the

output digital data signal), and wherein the signal generator means comprises clock signal regenerator means designed for deriving a digital clock signal from a digital data signal (see Fig. 12 and col. 13, lines 1-3).

Regarding claim 3:

Davis discloses the optical writing system according to claim 2, wherein the flipflop and the regenerator means are integrated into one unit (see Fig. 12; EFM decoder 1210 and flipflop 1212 are integrated into one unit within the signal processor 846).

Regarding claim 4:

Davis discloses the optical writing system according to claim 1, wherein the encoder device is designed to generate at its output a combined signal (EFM signal is a combined signal since the EFM decoder is able to recover clock signal from the EFM signal) which is based on a combination of a digital data signal and a digital clock signal, and wherein the signal generator means comprises demultiplexing means (see Fig. 12; the EFM signal inputted into the EFM decoder, which includes a demultiplexing means to derive a data signal and a clock signal) designed to regenerate a data signal (binary data signal) and a clock signal (channel clock signal) from a combined signal as coded by the encoder (also see col. 13, lines 1-3).

Regarding claim 5:

Davis discloses the optical writing system according to claim 4, wherein the flipflop and the demultiplexing means are integrated into one unit (see Fig. 12; EFM decoder 1210 and flipflop 1212 are integrated into one unit within the signal processor 846).

Regarding claim 6:

Davis discloses the optical writing system according to claim 1, wherein the signal generator means is arranged immediately before the flipflop device (see Fig. 12).

Regarding claim 7:

Davis discloses an optical recording apparatus for writing information to an optical storage medium, comprising an optical writing system according to claim 1 (see Fig. 8 and explanation in claim 1).

Regarding claims 8-10:

Claims 8-10 recite similar limitations as in claims 1 and 4; hence, Davis discloses all the limitations set forth in claims 8-10.

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Fujiwara et al. (US 5,561,652) is cited to show a reference that teaches using a flipflop in a recording pulse generating unit.

Fukuchi (US 2003/0210623) is cited because Fukuchi utilize an encoder to encode input signal prior to output the signal to the FIFO unit.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Lixi Chow whose telephone number is 571-272-7571. The examiner can normally be reached on Mon-Fri, 8:30am to 6:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wayne Young can be reached on 571-272-7582. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2627

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

LC 3/28/08

/Wayne R. Young/

Supervisory Patent Examiner, Art Unit 2627